

**REPLY UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
TECHNOLOGY CENTER 2800**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

|                              |   |                  |                                |
|------------------------------|---|------------------|--------------------------------|
| <b>Serial No.</b>            | 10/711,959                              | <b>Conf. No.</b> | 5958                           |
| <b>In Re Application of:</b> | Heng et al.                             | <b>Art Unit:</b> | 2825                           |
| <b>Filed:</b>                | 10/15/2004                              | <b>Dkt. #:</b>   | BUR920040201US1<br>(IBMB-0059) |
| <b>Title:</b>                | INTEGRATED CIRCUIT<br>SELECTIVE SCALING | <b>Examiner:</b> | Kik, Phallaka                  |

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AFTER-FINAL AMENDMENT**

Sir:

**I. INTRODUCTORY COMMENTS**

In response to the Final Office Action of January 05, 2007, please amend the above-referenced patent application as follows: